



Performance of UPFC to enhance Power Stability in Multi-Machine Power System under Fault Condition

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Abstract— Modern power systems are interconnected and exposed to external as well as internal disturbances. The stable operation of power system (Power System Stability) is the subject of great interest for power engineers for the last few decades. Stability in power system is achieved by many means, but nowadays FACTS controllers, which are a family of flexible alternating current transmission system devices, are widely used to enhance stability. Among these is the unified power flow controller (UPFC), the most versatile and prominent that controls the flow of active power and reactive power and also stabilize the voltage at selected bus. This paper presents an approach towards stability using UPFC in multi-machine power system. The proposed work is implemented in MATLAB/SIMULINK. The power system is simulated with and without UPFC to check the performance of the controller under fault condition. The simulation results show that in case when UPFC is used it takes much less time to enhance power stability after fault occurrence compared to the case without using UPFC.

Keywords— Power System Stability,FACTS,UPFC,Multi-Machine

I. INTRODUCTION

Electric Power system consists of generation, transmission, distribution and many ancillary devices such as controllers, regulators and protecting devices. Load is also an important part of the power system because the sole purpose of the main components is to feed the load. However there is often mismatch between supply and demand. This and the variable nature of load affect the performance and stability of power system. In power system there always arise the problem of stability. Stability is the set of measures taken to maintain the system components in synchronism. When fault occur in any part it will result in an abnormal flow of power, affects the voltage and also cause the system to oscillate. To damp out the oscillations and enhance stability, various types of FACTS devices are used [1]-[3]. Since the birth of AC system and problem of stability, a group of mechanical switches comprises of capacitor and reactors were used [4]. But there use is avoided due to their slow response. To overcome the deficiency the family of FACTS devices is introduced by EPRI (Electric Power Research Institute) in 1980[5]. According to the definition FACTS are power electronics fast

acting static switches that are used to control, design and operate the power system [6].

The organization of the paper is section I is the general introduction. Section II is about the structure and features of UPFC. The propos multi-machine power system is explained in section III. Whereas the simulation and results are illustrated in section IV. Finally section V concludes.

II. THE UPFC

UPFC is the most important member of FACTS devices family. It can enhance dynamic stability, transient stability and steady state stability [2]. Its unique feature is that it can control power flow, impedance, phase angle and voltage. This control of parameters can be selective or simultaneous [7]. Figure 1. shows the basic structure of UPFC. It consist of two converters, series and shunt transformer and a dc link capacitor.

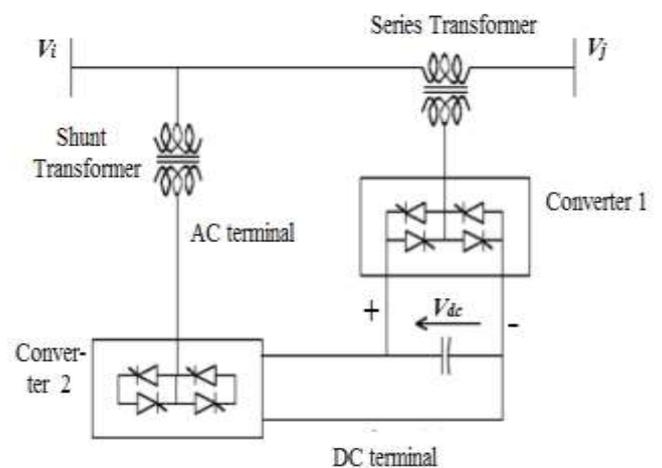


Figure 1. Configuration of UPFC

It combines the features of static synchronous series compensator (SSSC) and static synchronous compensator (STATCOM)[8]. The maximum defined voltage to transmission line is injected by series converter by adjusting phase angle from 0 to 2π . The injection is performed through boostable series transformer. This behaves as voltage source and can absorb or generate reactive power thus transferring

active/real power. The DC link maintains a constant voltage and provides path for the flow of active power. The primary purpose of shunt converter is providing real power to the series converter on demand. The shunt converter is capable of absorbing or generating reactive power and performing the function of STATCOM. The reactive power to the system is exchanged through both series and shunt converter. However, the real power is supplied through the DC link which is taken from the shunt converter.

III. POWER SYSTEM UNDER STUDY

The power system shown in Figure 2. is a multi-machine infinite bus system consisting of two generators G1 and G2 having a combined capacity of 1500 MW. Both generators are equipped with an excitation system, hydro turbine, and governor and PSS.

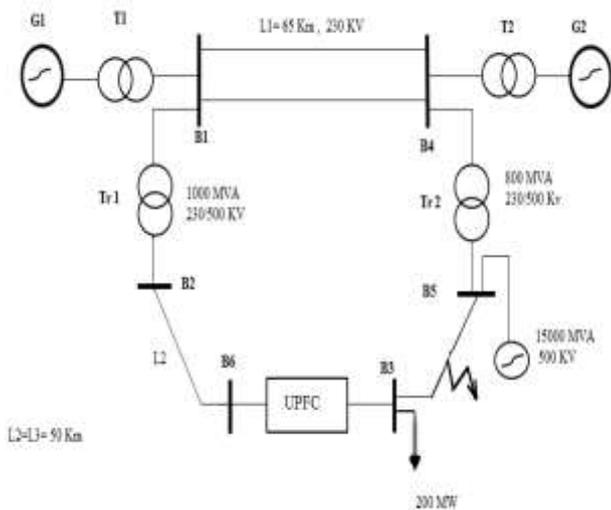


Figure 2. Single Line Diagram of Power System under study

The system is interconnected through six buses (B1-B6) and four step-up transformers T1, T2, Tr1 and Tr2. Tr1 is 1000 MVA, 230KV/500KV and connected between Bus1 and Bus 2. Tr2 is 800 MVA, 230KV/500KV and connected between Bus 4 and Bus 5. A load of 200 MW is connected to Bus 3. An equivalent of 15000MVA is connected to Bus 5 to balance the circuit. The system parameters and data are given in Appendix A.

IV. SIMULATION RESULTS AND DISCUSSION

The power system shown by the single diagram in Figure 2. is considered in the study. UPFC is connected between Bus 3 and Bus 6. A single phase fault of 100ms duration is created at Bus3.

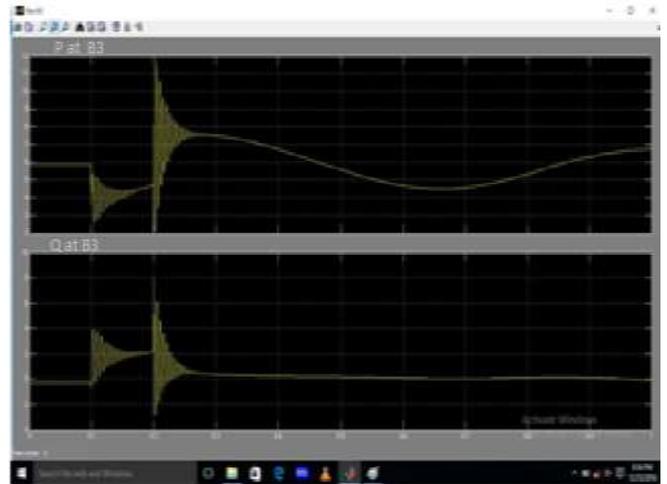


Figure 3. Single phase fault at Bus 3 without UPFC

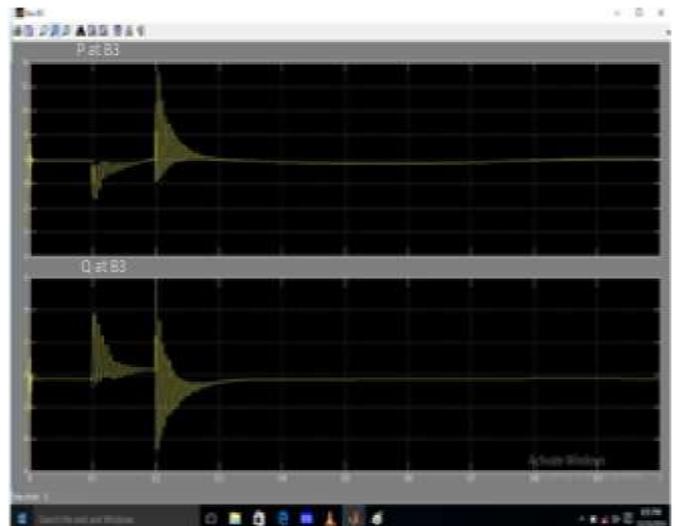


Figure 4. Single Phase fault at Bus 3 using UPFC

Pref and Qref values are set in the magenta block of the controller at Bus 3 in order to see the effect of the fault. Simulations are carried out in MATLAB/Simulink and the results are presented in the form of PQ waveforms. Figure 3 and Figure 4 illustrate that after the removal of the fault, the real power is stabilized in much less time in the presence of the controller, compared to the case when the controller is not used. Whereas the reactive power is immediately stabilized after removal of the fault in both cases.

V. CONCLUSION

In this paper, a multi-machine power system is presented. The performance of the UPFC is checked under fault conditions. The controller has the advantage to clear the fault quickly as

well as lower the settling time for power stability . From the simulation results it is clear that if the controller is used the fault is cleared in 0.1second and the real power stability is attained soon after the clearance of fault. Whereas it takes 1second to completely stabilizes the system if the controller is not used. In both cases the reactive power remain constant after the removal of fault.

APPENDIX A

Generators Parameters:

G1:500MW, Voltage(line-to-line)=13.8 KV

G2:1000MW, Voltage(line-to-line)=13.8 KV

Transformer Parameters:

Tr1:1000MVA, 13.8/230 KV

Tr2 : 1200MVA, 13.8/230 KV

Connection Type: Delta – Wye(Both)

UPFC Parameters:

500KV, 60Hz

Shunt Converter=100 MVA

Shunt Coverter Imp, R(pu)=0.22/30, L(pu)= 0.22

Series Converter= 100 MVA

Series Converter Imp, , R(pu)=0.16/30, L(pu)= 0.16

DC link voltage=40KV

DC link Total capacitance= 750e-6 F

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